

WHAT IS CLAIMED IS:

1. A charge pump type power supply circuit comprising:

a plurality of switches; and

a plurality of capacitors, wherein

a supply voltage is generated by switch controlling said plurality of switches and boosting the input voltage to a voltage of n times or $-n$ times the input voltage based on a power supply clock produced by an integrated circuit using a predetermined system clock, said integrated circuit being operated using said system clock.

2. A charge pump type power supply circuit according to claim 1, wherein

said integrated circuit suspends the generation of said power supply clock in response to a power save control instruction; and

said power supply circuit suspends the generation of said supply voltage in response to the suspension of said power supply clock.

3. A driving apparatus for a display device, comprising:

a driving circuit for generating a signal to allow a display section to display, said driving circuit being operated using a predetermined system clock; and

a charge pump type power supply circuit for generating a supply voltage for a display device by boosting the input voltage to a voltage n times or $-n$ times said input voltage, said power supply circuit including a plurality of switches and a plurality of capacitors; wherein

said driving circuit generates a power supply clock using said system clock; and

said power supply circuit generates said supply voltage by switch controlling said plurality of switches based on said power supply clock.

4. A driving apparatus for a display device according to claim 3, wherein,

said driving circuit suspends the generation of said power supply clock in response to a power save control instruction; and

said power supply circuit suspends the generation of said supply voltage in response to the suspension of the supply of said power supply clock.

5. A display device having a display section and a driving apparatus for driving the display section, wherein

said driving apparatus comprising:

a driving circuit for generating a signal to allow the display section to display, said driving circuit being operated using a predetermined system clock; and

a charge pump type power supply circuit for generating a supply voltage for said display device by boosting the input voltage to a voltage n times or -n times the input voltage, said charge pump type power supply circuit having a plurality of switches and a plurality of capacitors, wherein

said driving circuit further generates a power supply clock using said system clock and suspends the generation of said power supply clock based on a power save control instruction; and

said power supply circuit generates said supply voltage by

